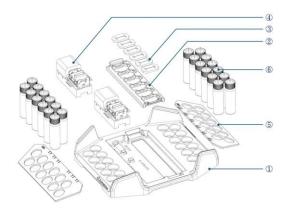
■ Specifications

System Component



- Base Platform
- 2 Chip Holder
- 3 Organ-on-chip

Pump

Sample Bottle Rack

Sample Bottle

Item

Dimensions

Weight

Chip Capacity

Pump Type

Flow Rate

Flow Rate Accuracy

Control Interface

Power Supply

Specification

211 x 250 x 76 mm

675 g ± 10%

Supports 6 chips

Dual peristaltic pumps

1.0 – 100.0 μL/min

± 20 % µL/min

Manual controller

AC 110 - 220V

Ready to Run.

Chip MPS Makes It Simple to Start.

Just power it up, run your workflow, and start generating results. Chip MPS brings Organ-on-Chip technology into your lab with the simplicity of everyday research tools.

CONTACT US

Schedule a collaboration test

Anivance Al



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BROCHURE

CHIP MPS

DYNAMIC PERFUSION CULTURE SYSTEM

Research Use Only.

► About Chip MPS

Chip MPS is a compact perfusion system designed to bring organ-on-chip research into everyday workflows. It supports long-term dynamic culture and real-time imaging, with modular upgrades that evolve with your research needs. Fully compatible with microscopes and standard lab equipment, it accelerates drug testing, disease modeling, and physiological analysis directly in your lab. Start faster, see more, and grow your experiments with confidence.

Key Features Plug-and-Play

1 LIGHTWEIGHT

Compact and under 1 kg, roughly the size of a laptop. Easy to carry, install, and relocate across workflows.

2 AFFORDABLE

Cost-effective and modular, offering affordable expansion tailored to your research needs.

3 SCALABLE

Chip MPS scales with your research through modular expansions that enable workflow customization.

Optional Modules Scalable for Applications



Aeromimic MPS

Supports aerosol exposure testing on lung-on-chip models

Enables evaluation of aerosolized drugs, nano-carriers, and airborne pathogens under dynamic airflow conditions.



Chip Holder

Designed for compatibility with confocal and high-content imaging systems

Ensures stable positioning of organ-on-chip devices during imaging, enabling high-resolution, automated data acquisition across multiple fields of view.



Customized Holder Design

Designed for flexible chip integration

Compatible with chips of varying sizes, thicknesses, and designs, including custom-built formats